

```

graph TD
    2[Generate Clocks] --> 4[Transmit Clocks To Receiving Device]
    4 --> 5[Generate Sync Signal]
    5 --> 6[Designate Sync Test Line]
    6 --> 8[Transmit Data Over Data Lines]
    8 --> 10[Receive & Sample Sync Signal At Receiving Device To Produce Sample Data Sets]
    10 --> 12[Compare Sample Data Sets To 1st Phase Sync Detect Patterns]
    12 --> 14[Store 1st Time Delay Values]
    14 --> 16[Compare Sample Data Sets To 2nd Phase Sync Detect Pattern]
    16 --> 18[Store 2nd Phase Time Delay Value]
    18 --> 20[Sample Future Data Streams On Designated Data Lines At 1st & 2nd Phase Time Delay Values]
    20 --> 22[Designate New Sync Test Line]
    22 --> 24[Calculate Associated 1st & 2nd Phase Time Delay Values For Each Data Line]
    24 --> 26[Sample Data Streams at Associated 1st & 2nd Phase Time Delay Values]
  
```

Fig. 1

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graph TD
    30[Generate Clocks] --> 32[Transmit Clocks]
    32 --> 34[Designate Sync Test Line & Data Skew Reference Line]
    34 --> 36[Transmit Sync Pattern]
    36 --> 38[Calculate 1st & 2nd Phase Delay For Sync Test Line]
    38 --> 40[Designate Next Sync Test Line]
    40 --> 42[Transmit Sync Pattern On Next Data Line At Pre-Determined Number Of Clock Cycles After Transmission Of Previous Sync Test Line]
    42 --> 44[Calculate Data Skew Of New Sync Test Line With Respect To Data Skew Reference Line]
    44 --> 46[Repeat For Each Data Line]
    46 --> 48[Correct For Data Skews]

```

Fig. 2

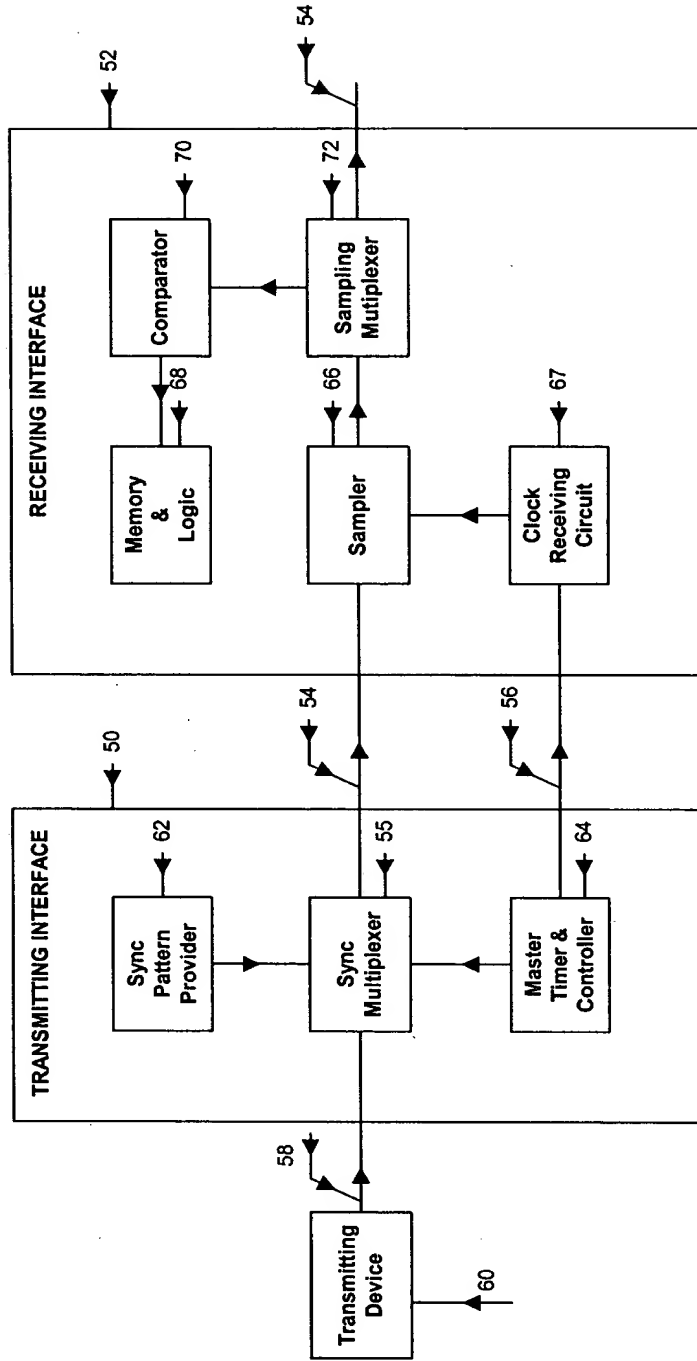


Fig. 3

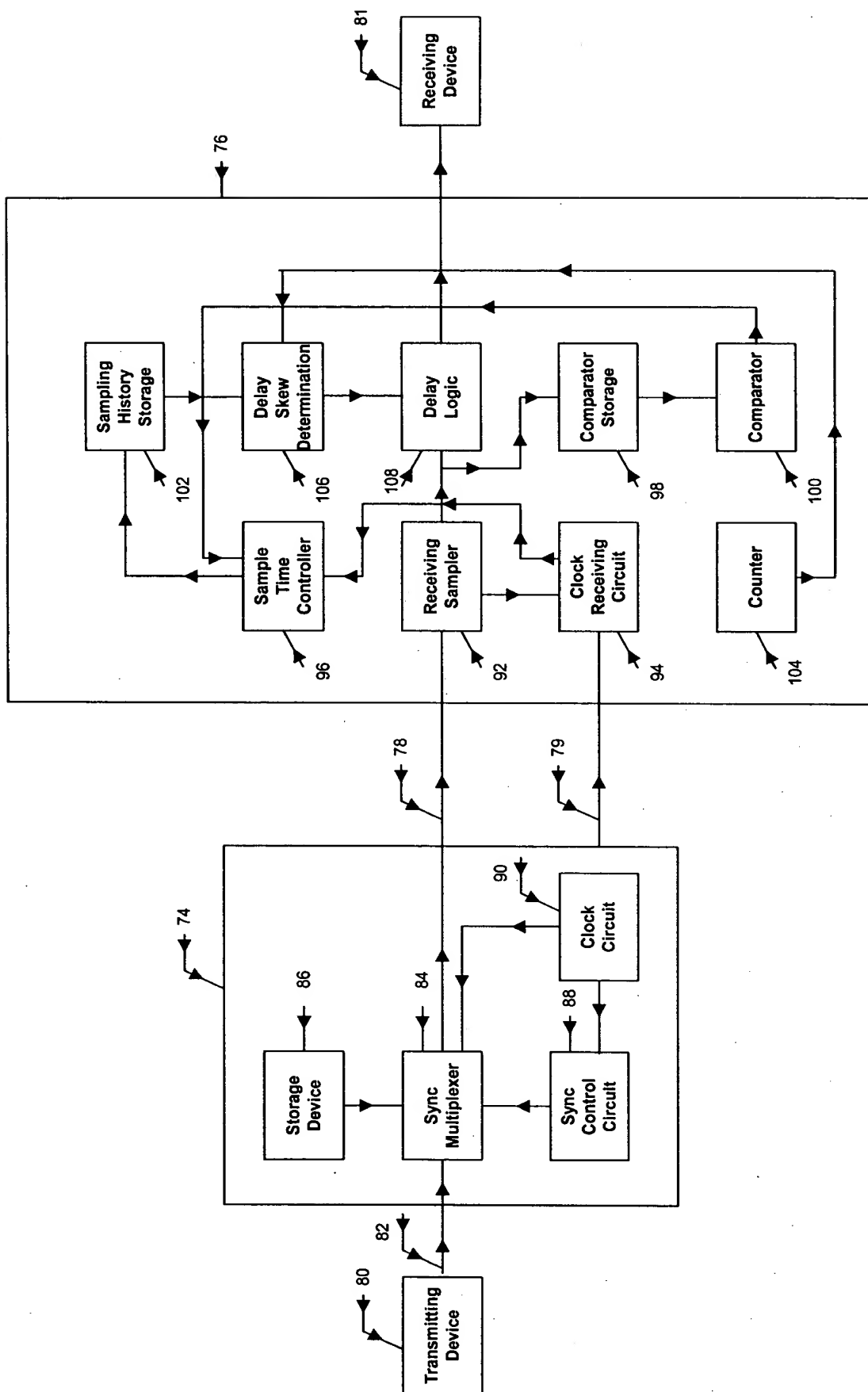


Fig. 5

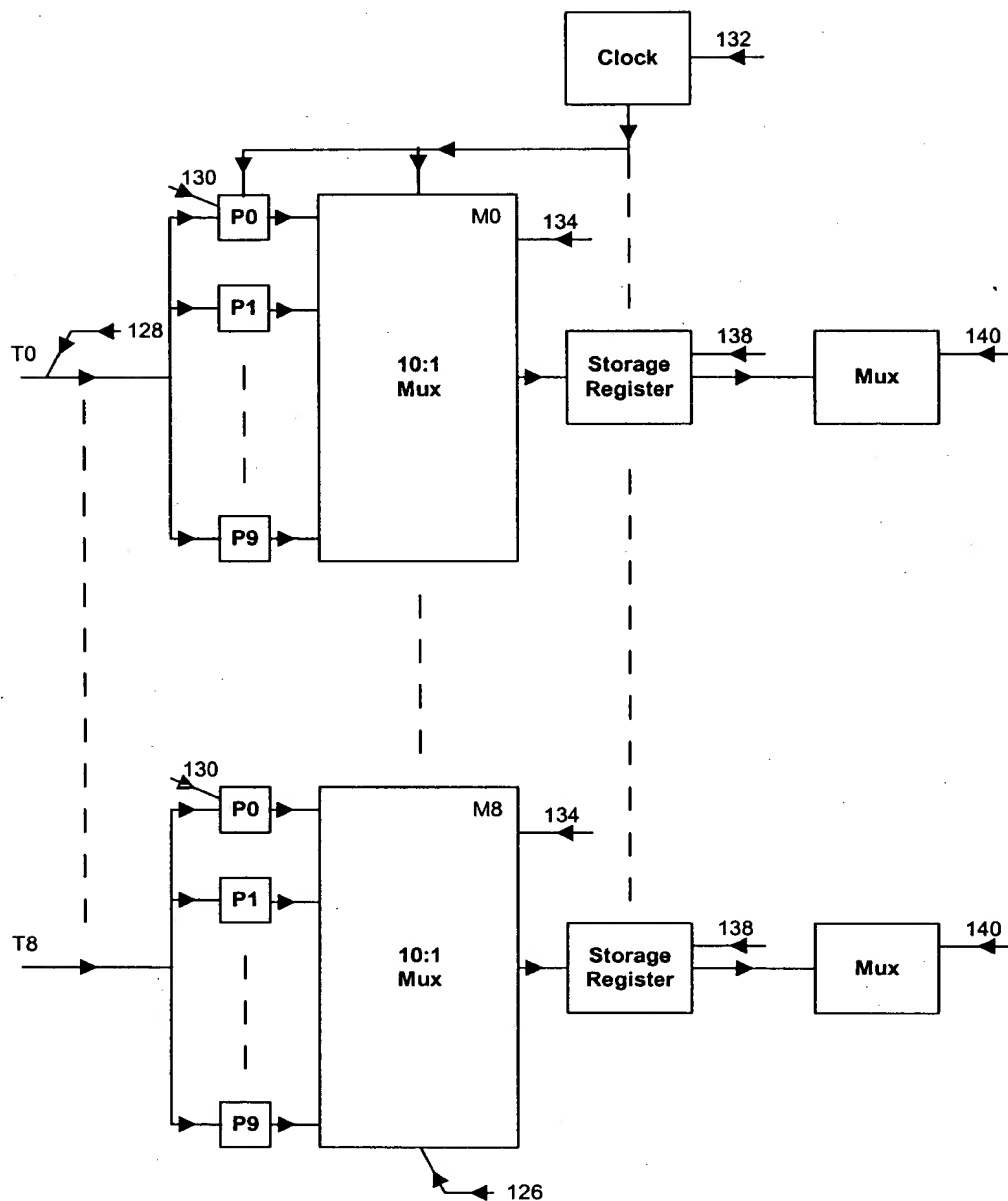


Fig. 6